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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/813,907	03/30/2004	Christopher J. Diorio	IMPJ-0027A	5046	
49684	7590 05/31/200	EXAMINER			
IMPJ - THELEN REID BROWN RAYSMAN & STEINER LLP P.O. BOX 640640			PHAN, TI	PHAN, TRONG Q	
SAN JOSE, CA 95164-0640		ART UNIT	PAPER NUMBER		
			2827		
			MAIL DATE	DELIVERY MODE	
			05/31/2007	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.



		Application No.	Applicant(s)		
Response to Rule 312 Communication		10/813,907	DIORIO ET AL.		
		Examiner	Art Unit		
		TRONG PHAN	2827		
The MAILING DATE of this communication appears on the cover sheet with the correspondence address –					
1. 🛭 The	amendment filed on <u>15 May 2007</u> under 37 CFR 1	.312 has been considered, an	d has been:		
a) 🛚	entered.				
b) 🗌	entered as directed to matters of form not affecting the scope of the invention.				
c) 🗀	disapproved because the amendment was filed after the payment of the issue fee. Any amendment filed after the date the issue fee is paid must be accompanied by a petition under 37 CFR 1.313(c)(1) and the required fee to withdraw the application from issue.				
d) 🗀	disapproved. See explanation below.				
e) 🗌	entered in part. See explanation below.	Phantro	ry		
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DAVID B. RITCHIE THELEN REID BROWN RAYSMAN & STEINER LLP SERIAL NO.: 10/813,907

OUR REFERENCE NO.: IMPJ-0027A (033327-054)

REPLACEMENT SHEET

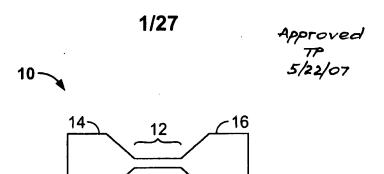


FIG. 1 (Prior Art)

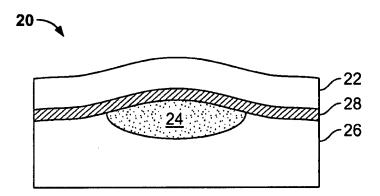


FIG. 2 (Prior Art)

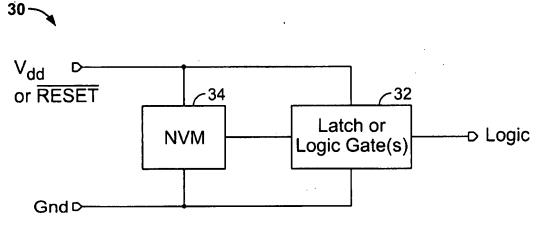
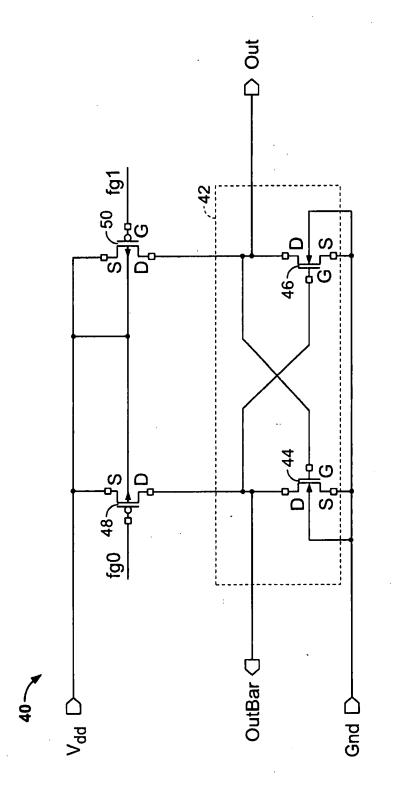


FIG. 3

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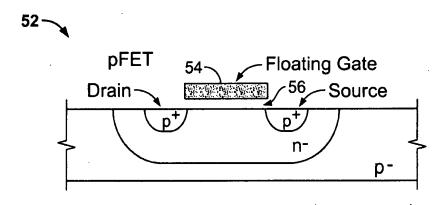


FIG. 5A

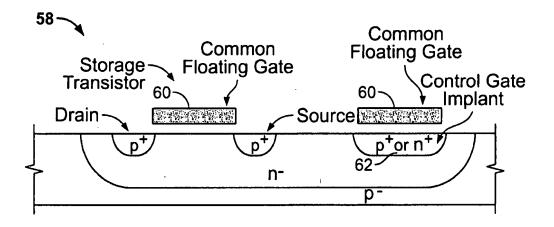


FIG. 5B

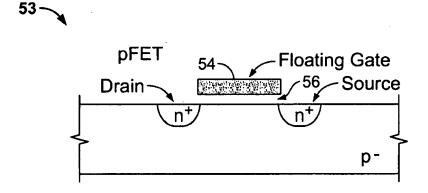


FIG. 5C

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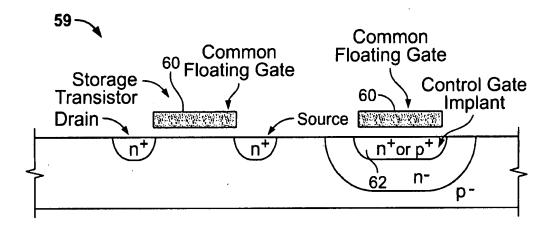


FIG. 5D

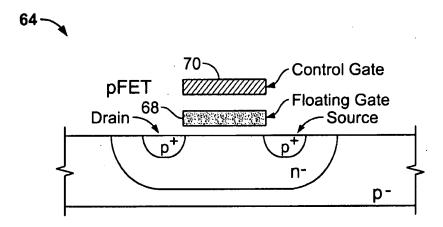


FIG. 5E

REPLACEMENT SHEET

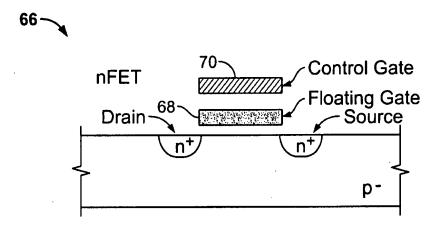


FIG. 5F

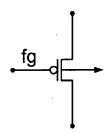
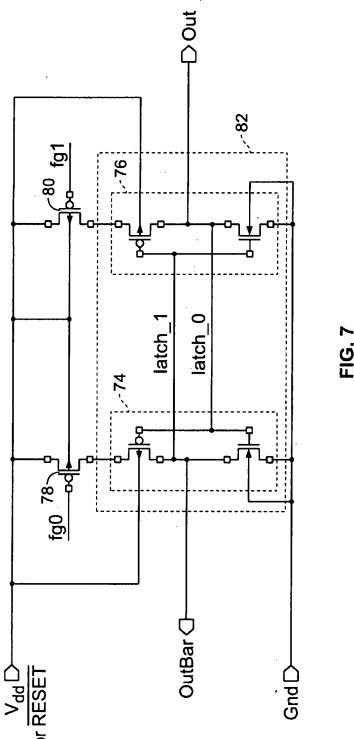
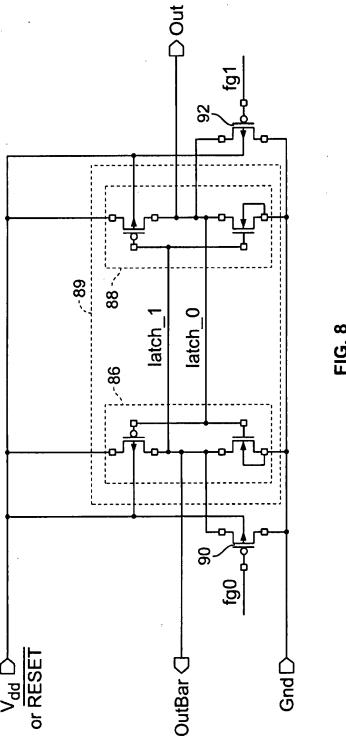


FIG. 6

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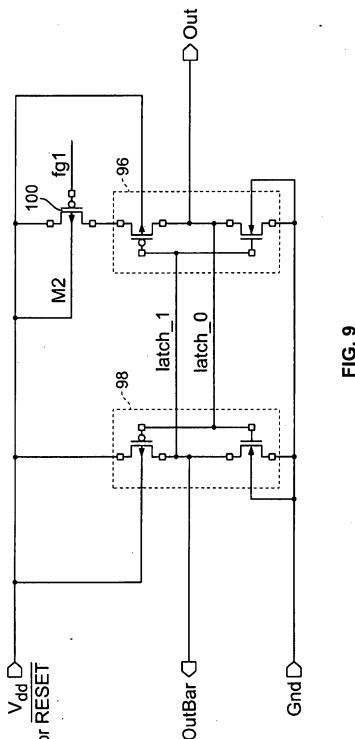


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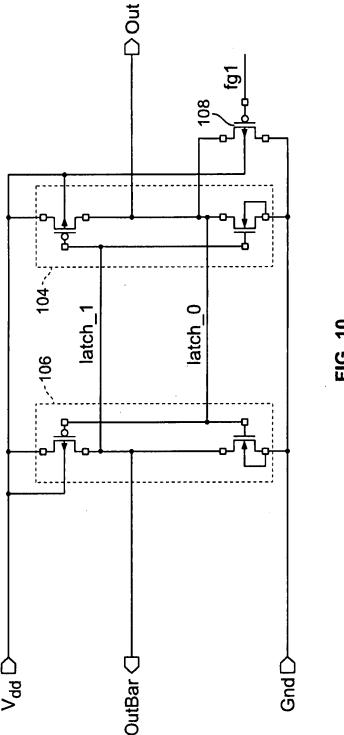


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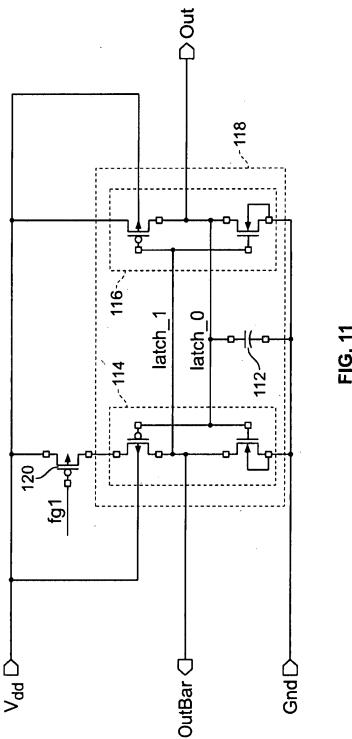
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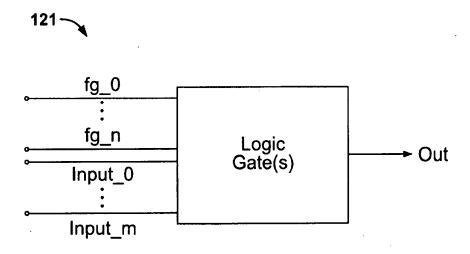


FIG. 12A

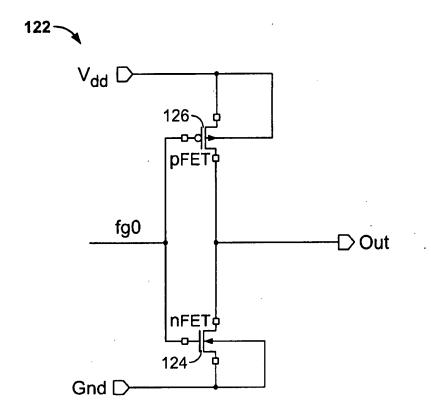


FIG. 12B

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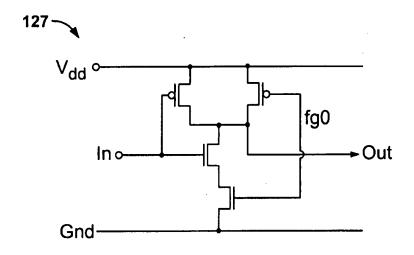


FIG. 12C

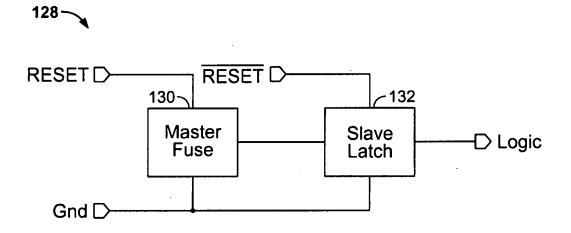
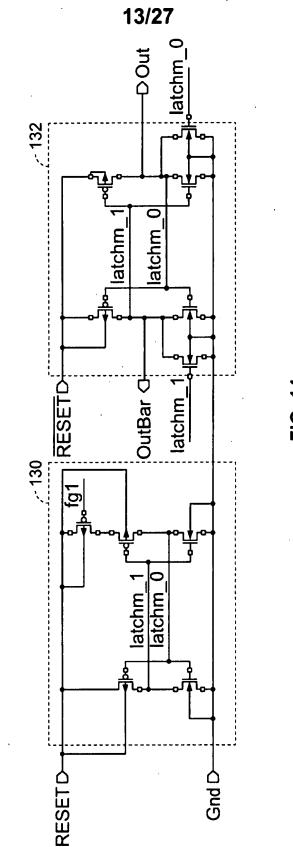
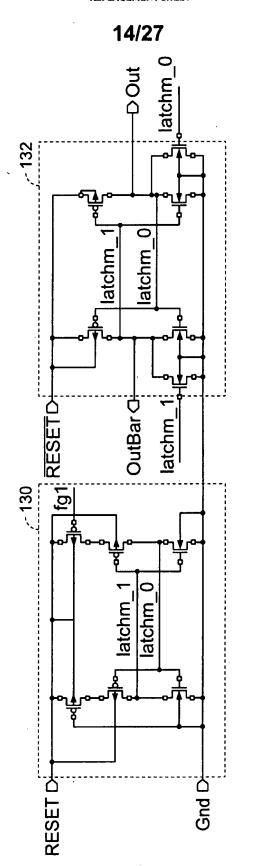
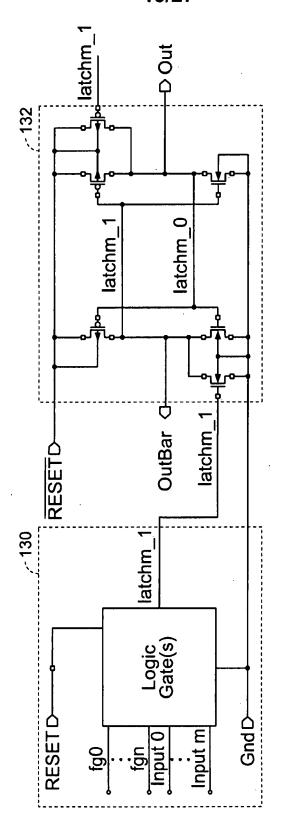


FIG. 13







<u>16. 16</u>

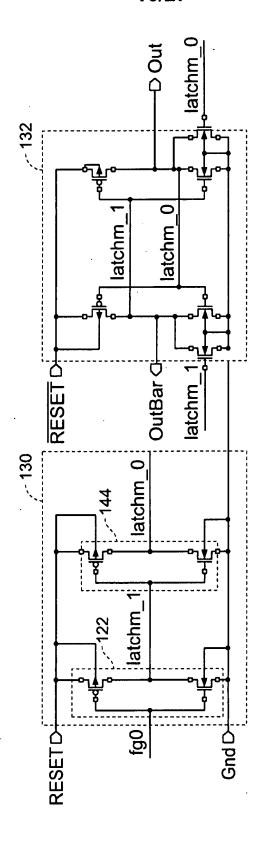
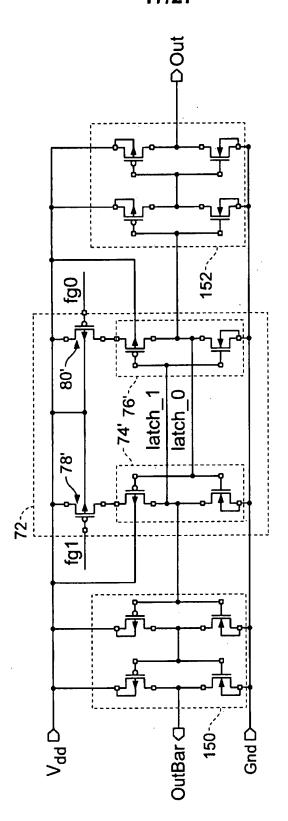


FIG. 17



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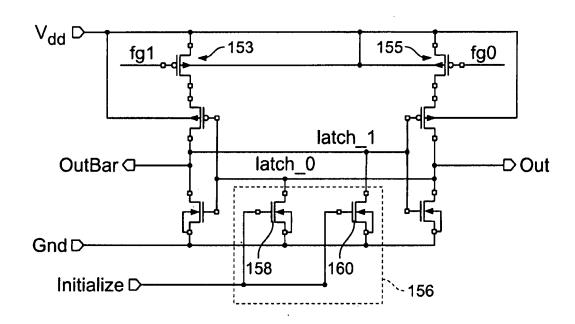


FIG. 19

REPLACEMENT SHEET

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MOSCAP Tunneling Junction

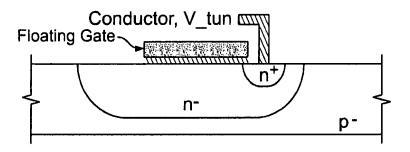


FIG. 20

pFET Tunneling Junction

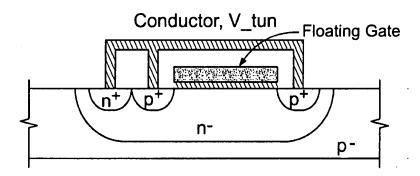


FIG. 21

REPLACEMENT SHEET

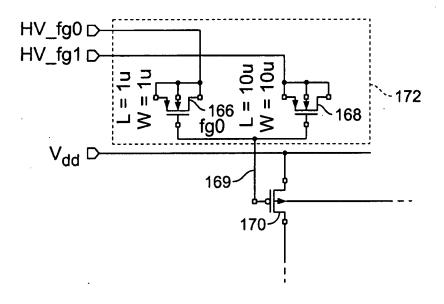
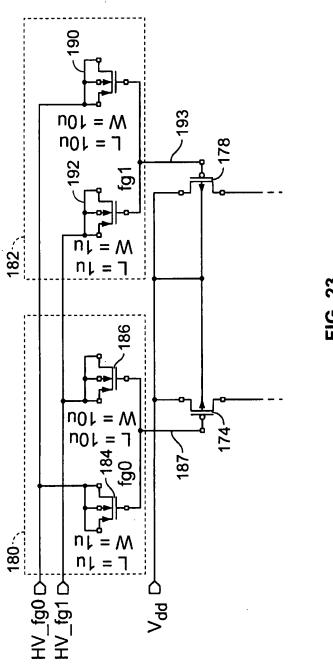


FIG. 22

REPLACEMENT SHEET



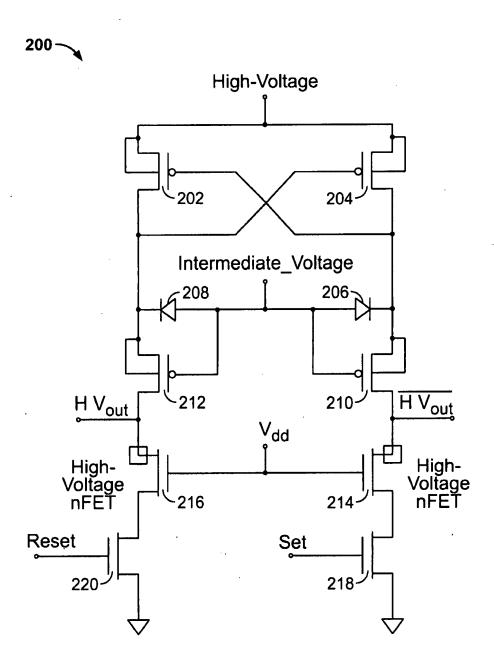


FIG. 24

- REPLACEMENT SHEET

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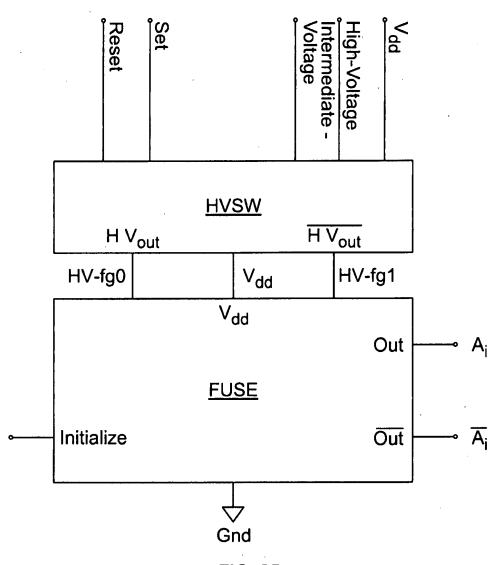
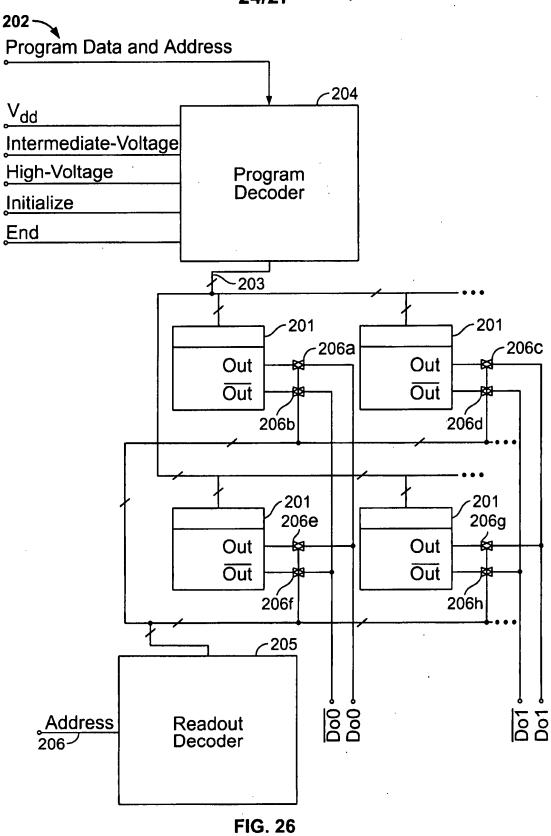
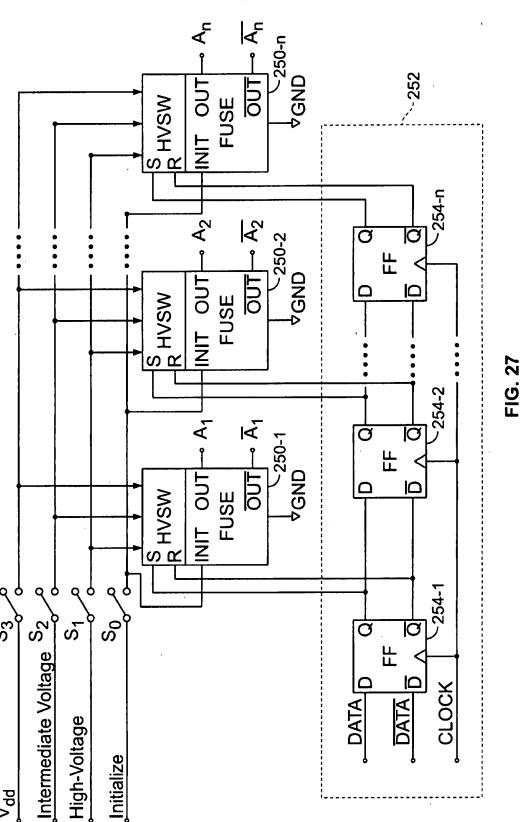


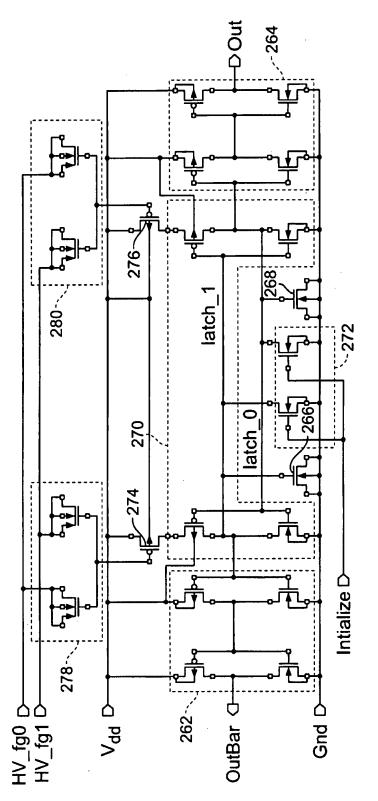
FIG. 25







REPLACEMENT SHEET



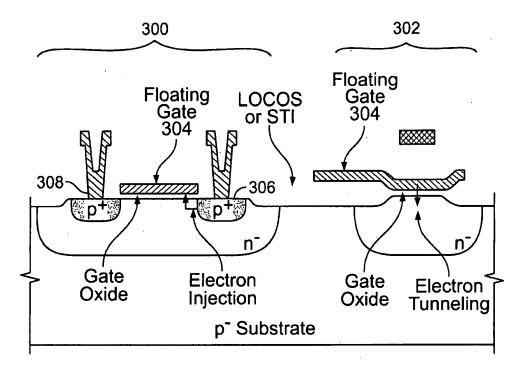


FIG. 29A

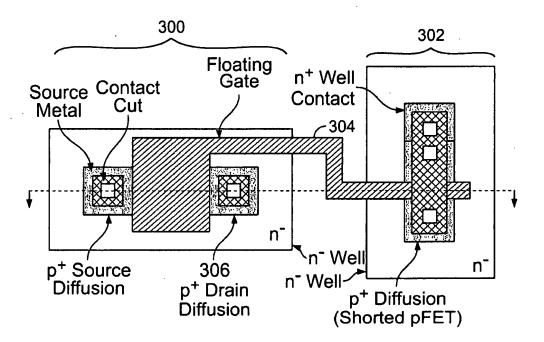


FIG. 29B